Radiation detectors

Presentation material

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Introduction

To begin with, we have chosen this topic due to the fact that silicon radiation detectors are one of the main type of particle detectors used in the radiation detection industry nowadays. Hence, it is crucial for an engineer in this field to know the history of silicon production as well as the development process and concrete examples of the different types of detectors.

In this wiki page we are studying the rapid progress of the silicon radiation detectors, the design of the single sided stereo angle silicon strip detector, cadmium telluride (CdTe) and cadmium zinc telluride (CdZnTe) or CZT pixel detectors and the development of the detectors in Finland. The information was collected from Institute of Electrical and Electronics Engineers/Institution of Electrical Engineers (IEEE/IEE) Electronic Library online.

History of silicon radiation detectors
According to Rehak (2003), silicon is the main material which is used in electronics and radiation detection industries. Moreover, there was a rapid development in the field of silicon radiation detectors in the last decade, which could be noted as a considerable progress after 1982, when planar technology was invented. [1.] Furthermore, according to Orava et al (2007), even today new development activity is initiated with introduction of 3D detector technologies, active edge silicon processing and other perspective inventions in the field. [3.] It has to be said that there is always interaction between electronics industry and silicon radiation detectors because they both use silicon as the main component. The main point is that silicon used for radiation detectors should have 3-4 times more resistivity and lower density of traps than the one used for electronics. [1.]

Interestingly, almost all radiation detectors before 1982 were lithium-drifted (Figure 1). Lithium doping was used in order to increase the thickness of the detector and thus to decrease the capacitance and apply reasonable voltages which would lead to lower noise levels.

![Figure 1. Lithium drifted silicon detector. Reprinted from Rehak (2003) [1,2494].](image)

However, the main disadvantage was the instability of the surfaces along the field of the detector and working temperature of about 77K. Obviously, silicon radiation detectors which could be used at room temperature were drastically needed. [1.]

Planar technology was introduced in 1982, which led to the rapid progress in development of the silicon detectors. Among others, strip detectors were invented and matched to meet the requirements of the Large Hadron Collider (LHC) developed at The European Organization for Nuclear Research (CERN). Main advantages were lower noise, smaller capacitance, room working temperature, easier production, reduced cooling requirements and larger area coverage. [1,3]

**Strip silicon radiation detectors**
Strip silicon detectors consist of n type material while having p type aluminium strips on the surface which are separated by a thin insulator. At the same time there is an electric field between the p strips and the n type material. When a radiation particle passes the detector, silicon atoms are ionized and free electrons leave these atoms with an electron vacancy (holes). After they reach the p type strip they are collected and then create a charge on the aluminium strip which is possible to be measured with special electronics.

*Orava et al (2007)* proposes a new type of the strip silicon detector (Figure 2) which implements cross-connected short strips interlaced by long strips. The main advantage is that better measuring resolution is achieved, which requires much less accuracy [3].
Figure 2. Layout of the single sided stereo angle silicon strip detector. Reprinted from Orava et al (2007) [3,227].

Furthermore, detector is designed in a way that every second strip is long while short strips are crossing them which helps to register particles at better angles to the surface of the sensor. Consequently, the high efficiency and proper special resolution are achieved. [3].
CdTe and CdZnTe pixel detectors

Cadmium telluride (CdTe) is a stable crystalline compound composed from cadmium and tellurium. Doped with a small amount of Zn it produces cadmium zinc telluride, (CdZnTe) or CZT. CdTe and CdZnTe pixel detectors are implemented in imaging systems which are widely used in medical diagnostics (mammography, dental phantoms), astronomy, and industrial research. CdTe and CdZnTe pixel detectors are very appealing due to their high energy resolution at normal conditions which is not high enough in currently implementing materials [4, 1]. Another advantage of those detectors is their perfect efficiency while working with X-ray energy spectrum as well as compatibility with gamma rays, alpha and beta particles [5, 3]. Nowadays, there are two ways of using CdTe/CZT detectors. The first one is focused on single channel detectors which are used in X- and gamma- ray spectroscopy. The second is proposed for multi-channel devices in nuclear medicine mostly. [5, 26]

Materials and manufacturing technologies

Strip silicon detectors consist of n type material while having p type aluminium strips (which are used to collect the charge) on the surface which are separated by a thin insulator. The crystalline structure is diamond cubic (FCC). Polysilicon consists of small Si crystals randomly oriented.

There are two types of material used for package of detector: ceramic or polymer. Some materials are more favorable than others due smaller capacitance, lower noise, room working temperature, easier production, reduced cooling requirements and larger area coverage.

Typical manufacturing process steps (Figure 13):
1. Starting Point: single-crystal n-doped wafer. Wafer is a very thin slice of silicon crystal which is used to produce the actual detectors.
2. Surface passivation by SiO2-layer. E.g. growing by (dry) thermal oxidation at 1030 °C.
3. Window opening using photolithography technique with etching, e.g. for strips
4. Doping using either
   - Thermal diffusion (produce a thin layer of oxide on the surface of a wafer)
   - Ion implantation (introduction of dopants in a semiconductor by accelerating ions)
5. After ion implantation: Curing of damage via thermal annealing at approx. 600°C, (activation of dopant atoms by incorporation into silicon lattice)
6. Metallization of front side: sputtering or CVD (chemical vapor deposition) to produce thin film
7. Removing of excess metal by photolithography: etching of non-covered areas
8. Full-area metallization of backplane with annealing at approx. 450°C for better adherence between metal and silicon
9. Wafer dicing (cutting) [12].
Figure 13. Manufacturing process steps. Reprinted from: http://indico.cern.ch/event/124392/contribution/0/material/slides/0.pdf [12].
Silicon radiation detectors characteristics

As an example, we have considered datasheets of 2 sensors. Those are DT Standard Photodiode Products [7] and First Sensor PIN PD [8].

Radiation detectors are usually very high sensitive, though the exact value is not defined. It can be improved by depressing noise signals which appear from e.g. light. Manufacturing material's quality is very important in sensitivity rise.

Although it is impossible to set the exact mathematical equation to introduce the transfer function of the sensors (nor do the datasheets give any transfer functions), it can be defined after measuring and analyzing spectrum. The maximum efficiency for both sensors is achieved at 10 keV energy level (Figure 3, Figure 4).

![Graph](http://www.deetee.com)

Figure 3. X-ray Efficiency vs. Energy in XRB series Diodes. Reprinted from http://www.deetee.com [7].
To evaluate radiation sensor it is important to consider its major characteristics:

1. Active area
2. Dark current
3. Capacitance
4. Shunt resistance

To understand those characteristics, there is a short explanation for each. Active area is a part of a sensor which contains the transistors, resistors and capacitors, and perform the actual detection and storage operations. Dark current is a small current which flows through the sensor because of the random generation of the disordered location of electrons and holes in the depletion region. Capacitance is an electrical characteristics which appears by applying an electrical charge to two conductive objects with a
time period between them. Shunt resistance is a low-resistance connection between two points that forms an alternative path for a portion of the current.

Moreover, it is necessary to notice the operation characteristics to avoid the breakage of the sensor. Operating voltage and temperature, as well as storage temperature are crucial (Figure 5, Figure 6).

<table>
<thead>
<tr>
<th>Characteristics</th>
</tr>
</thead>
<tbody>
<tr>
<td>Type</td>
</tr>
<tr>
<td>series, size, shape, package (see below)</td>
</tr>
<tr>
<td>XRB 100s-CB380</td>
</tr>
</tbody>
</table>

Figure 5. XRB 100s-CB380 characteristics. Reprinted from http://www.deetee.com [7].

<table>
<thead>
<tr>
<th>Symbol</th>
<th>Characteristic</th>
<th>Test Condition</th>
</tr>
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<tbody>
<tr>
<td>I&lt;sub&gt;D&lt;/sub&gt;</td>
<td>Dark current</td>
<td>V&lt;sub&gt;R&lt;/sub&gt; = 12 V</td>
</tr>
<tr>
<td>T&lt;sub&gt;K&lt;/sub&gt;(I&lt;sub&gt;D&lt;/sub&gt;)</td>
<td>Temperature coefficient</td>
<td>V&lt;sub&gt;R&lt;/sub&gt; = 12 V; change of dark current</td>
</tr>
<tr>
<td>C&lt;sub&gt;e&lt;/sub&gt;</td>
<td>Capacitance</td>
<td>V&lt;sub&gt;R&lt;/sub&gt; = 0 V; f = 10 kHz</td>
</tr>
<tr>
<td>V&lt;sub&gt;R&lt;/sub&gt;</td>
<td>Rise time</td>
<td>V&lt;sub&gt;R&lt;/sub&gt; = 12 V; E = 10 keV; R&lt;sub&gt;L&lt;/sub&gt; = 50 Ω</td>
</tr>
<tr>
<td>Shunt Resistance</td>
<td>V&lt;sub&gt;R&lt;/sub&gt; = 10 mV</td>
<td></td>
</tr>
<tr>
<td>Noise current</td>
<td>V&lt;sub&gt;R&lt;/sub&gt; = 12 V</td>
<td></td>
</tr>
<tr>
<td>V&lt;sub&gt;BR&lt;/sub&gt;</td>
<td>Breakdown voltage</td>
<td>I&lt;sub&gt;R&lt;/sub&gt; = 2 μA</td>
</tr>
</tbody>
</table>

Figure 6. First Sensor PIN PD characteristics. Reprinted from http://www.marubun.co.jp [8].

**Interface electronic circuits**
As almost all other sensors, radiation detectors need to be interfaced before being able to process the data. An interface or a signal conditioning circuit has a specific purpose: to bring the signal from the sensor up to the format which is compatible with the load device [10]. Cremat Inc. radiation detection electronics can be used with a wide range of detectors, including compound semiconductor radiation detectors, scintillator-photodiode detectors, avalanche photodiodes etc [9].

Typical interface electronic circuit using Cremat amplifiers is presented in Figure 7.

![Figure 7. General schematic. Copied from http://www.cremat.com][9].

The Cremat's CR-110 is a single channel charge sensitive preamplifier module intended for use with various types of radiation detectors including semiconductor detectors. Bias voltage can be used if required. Radiation is detected as a series of pulses, resulting in fast bursts of current (from 1 ns to few μs) flowing into preamplifier input. After the series of amplifiers it flows into the Cremat's CR-200 Gaussian shaping amplifier. The CR-200 is a single channel shaping amplifier, which is processing the signals from charge sensitive preamplifiers. It accepts a step-like input pulse and produces an output pulse shaped like a Gaussian function (Figure 8). [9.]
The main aim of this is to filter the noise from the signal and to provide a quickly restored baseline. The CR-200 shaping amplifiers have low output impedance (<5 Ohms) and can source/sink 10 mA of output current. CR-110 output impedance is 50 Ohms.\[9.\]

The whole circuit is presented as commercially available PCBs: CR-150 evaluation board and CR-160 Gaussian shaping amplifier evaluation board (Figure 9, Figure 10).
Figure 9. CR-150 evaluation board. Copied from http://www.cremat.com [9].
The CR-160 includes an adjustable low-noise wide-band amplifier having gains adjustable from 0 to 100. Combined with the CR-200-X gain of 10, this allows an overall gain of 0 to 1000. Furthermore, an inverted-polarity signal is available, as well as adjustments for pole-zero correction and DC offset.[9] In order to calibrate the sensor there is a number of potentiometers: Fine Gain to adjust the gain from 0 to 100; Pole-Zero to adjust the baseline of the output; Offset Adjustment; Signal Polarity;

The whole system has analog nature without any conversion to digital format. A/D conversion is typically happening in PC or various embedded systems. For instance, pulse height analyzer (PHA) is an instrument used in nuclear and elementary particle physics research which accepts electronic pulses of varying heights from particle and event detectors, digitizes the pulse heights, and saves the number of pulses of each height in registers or channels for later spectral analysis.

Last but not least, there is a possibility to read out the radiation signal when multichannel detectors are used using application-specific integrated circuits (ASIC). An example is AGIPD: Adaptive Gain Integrated Pixel detector (Figure 11).
AGIPD detector consists of 64x64 pixels sensor array which is connected to ASIC circuit (Figure 12).

![ASIC Pixel](image)

Inside the ASIC there are added capacitors which help to minimize the noise. The stored analog value is multiplexed and transferred then to ADCs. After that the signal is proceed to column bus from where it goes to DAQ system. [11.]

**Development in Finland**

Today there are two big companies which implement CdTe/CZT detectors in Finland.
AJAT Ltd. designs and manufacture imaging systems based on those detectors. They use new mechanism of straight conversion on the signal from detector to read out CMOS chips. AJAT products are integrated by OEM partners from different areas: from medicine and dental care to industrial 3D imaging. [6].

Examples of AJAT products:

- ART Plus Panoramic Imaging System (Dental care)
- Industrial CT and laminography for inspection of pipes in nuclear power stations (Industry)
- Bone densitometry (Medicine). [6].

VTT is the second company which uses CdTe/CZT detectors. They provide patterning method of the thick diffused junctions on as well as Hybrid CdTe-CMOS Semiconductor Radiation Detectors. Unfortunately, VTT do not disclose their patents’ information widely so there are only basic properties of the technology are known.

VTT Technical Research Centre of Finland has been developing strip silicon radiation detectors for many years. What is more, VTT have been closely working with CERN which conducts the most important experiments on particle physics in the world.

The production line at VTT can make wafers up to 200 mm in diameter using planar technology. Furthermore, the thickness of the strip detectors is only 150 m or 300 m which can be implemented for different bias configurations. Area of the detectors is from 5 x 5 cm$^2$ to 1 x 1 cm$^2$. Interestingly, the modern fabrication techniques such as etching, planarization, lithography and bonding are applied. The most important thing is that leakage currents of the detectors are under few nanoamperes, which makes it acceptable for radiation detection. [2].

References


6. AJAT Ltd. official webpage


7. Detection Technology Oy official webpage
8. Marubun Corporation official webpage

URL: http://www.marubun.co.jp/product/measurement/sensor/8ids6e000000k1lh-att/X100-7SMD.pdf, Accessed 5 April 2014.

9. Cremat Inc. official webpage


